



Appl. No. 09/322,259

#22  
VT  
4/18/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : John Terrance Reagan :  
Appl. No. : 09/322,259 : Grp./Art Unit : 2654  
Filed : 28 May 1999 : Examiner: M. Opsasnick  
Title : Reducing Artifact Generation In :  
A Vocoder (as amended) :  
  
Docket No. : PHA51214 (New) :  
VLSI-3410 (Old) :

RECEIVED

FEB 21 2003

Assistant Commissioner for Patents  
Washington DC 20231

Technology Center 2600

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith are three sheets of formal drawings which include Figs. 1-3  
of the above-reference application.

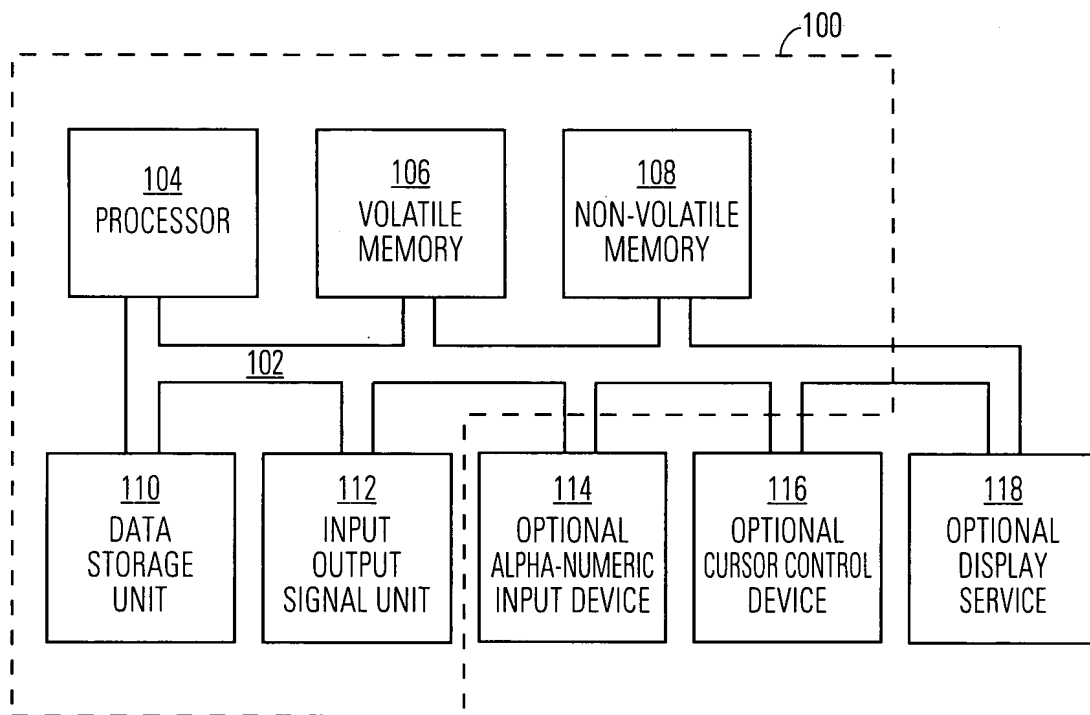
Respectfully submitted,

By Raymond J. Werner  
Raymond J. Werner  
Reg. No. 34,752

Dated: 10 February 2003  
Portland, Oregon



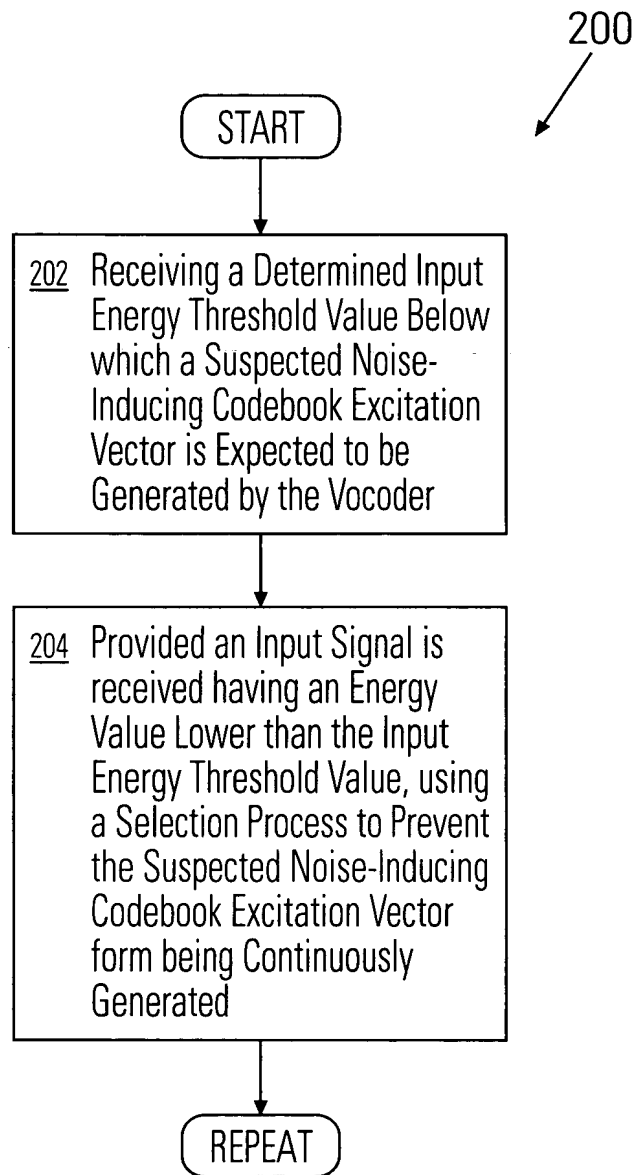
1/3



**FIG. 1**



2/3



**FIG. 2**

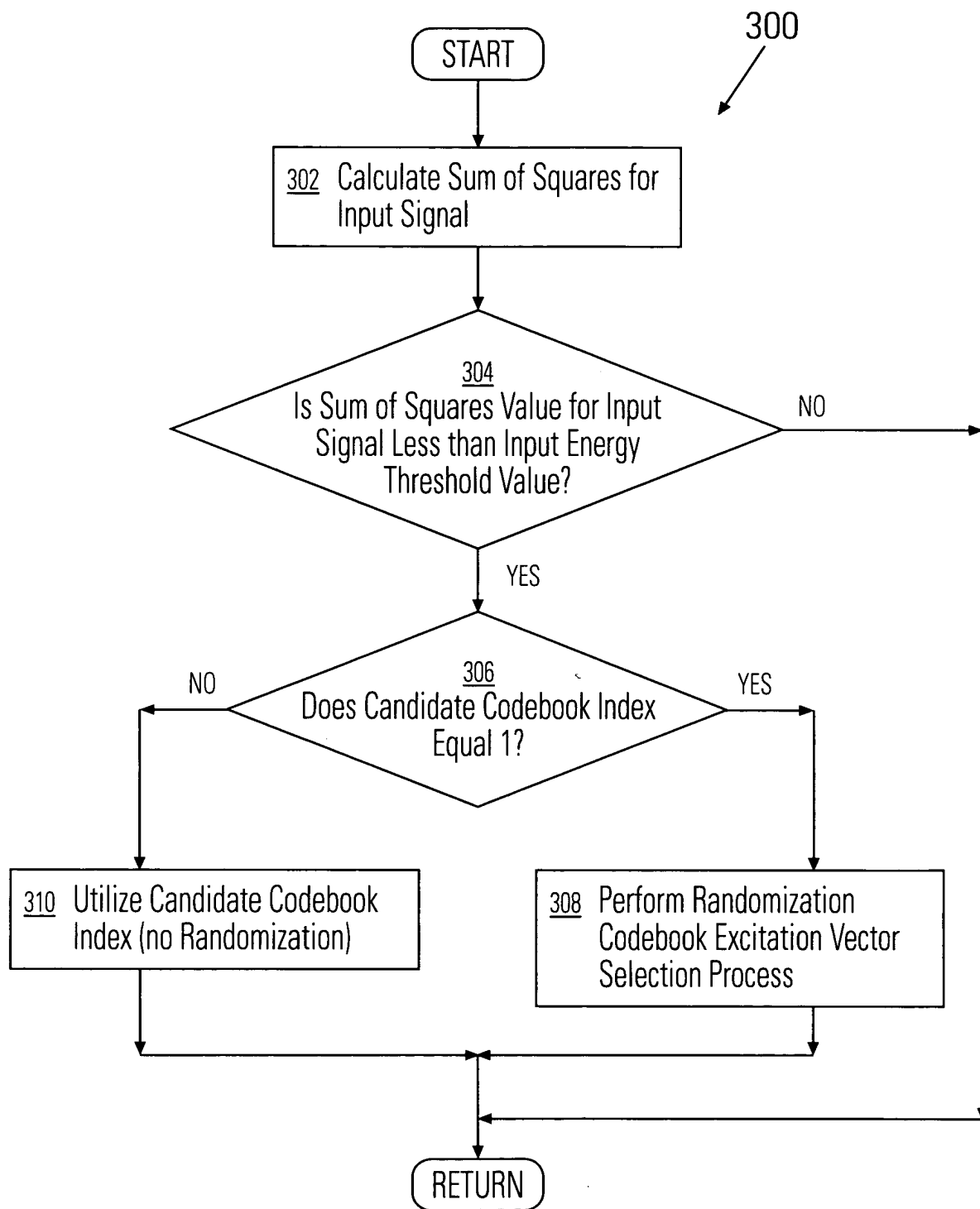


FIG. 3